

TITLE OF INVENTION

**PRINTED WIRING BOARDS HAVING CAPACITORS
AND METHODS OF MAKING THEREOF**

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ABSTRACT

In a printed wiring board, a plurality of stacked innerlayer panels have capacitors connected in parallel by connecting a first electrode of a first panel with a first electrode of a second panel, and similarly connecting second electrodes of the first and second panels. The innerlayer panel
10 having capacitors connected in parallel provides a high capacitance in a small x-y area. An alternate printed wiring board has a capacitor having a first foil electrode, and second and third electrodes located on opposite sides of the first foil electrode. Yet another printed wiring board has capacitors formed as an array of discrete foil electrodes spaced from an
15 array of discrete printed electrodes. Forming discrete interconnected electrodes allows the electrodes to be fired without excessive thermal coefficient of expansion stresses damaging the electrodes.